



PATENT

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

In re Patent Application of

Wipawan Yindeepol et al.

Application No. 10/041,112

Filed: January 8, 2002

For: ZENER TRIM USING DOUBLE POLY  
PROCESS

Group Art Unit: 2815

Examiner: Baumeister, B.W.

INFORMATION DISCLOSURE  
STATEMENT

MAR 02 2004

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Sir:

Applicants submit herewith information of which applicants are aware, which applicants believe may be material to the examination of this application and in respect of which there may be a duty to disclose in accordance with 37 CFR § 1.56.

This Information Disclosure Statement:

- (a) ☐ accompanies the new patent application submitted herewith. 37 CFR § 1.97(a).
- (b) ☐ is filed within three months after the filing date of the application or within three months after the date of entry of the national stage of a PCT application as set forth in 37 CFR § 1.491.
- (c) ☐ as far as is known to the undersigned, is filed before the mailing date of a first Office Action on the merits.

**CERTIFICATE OF MAILING**

I hereby certify that this correspondence is being deposited with the United States Postal Service, postage prepaid, in an envelope, addressed to Mail Stop \_\_\_\_\_, Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450 on 02-19-04

Dated: Feb. 19, 2004 By: [Signature]

(d) ☒ is filed after the first office action and more than three months after the application's filing date or PCT national stage date of entry filing but, as far as is known to the undersigned, prior to the mailing date of either a final rejection or a notice of allowance, whichever occurs first, and is accompanied by either the fee (\$180) set forth in 37 CFR § 1.17(p) or a certification as specified in 37 CFR § 1.97(e), as checked below.

(e) ☐ is filed after the mailing date of either a final rejection or a notice of allowance, whichever occurred first, and is accompanied by the fee (\$180) set forth in 37 CFR § 1.17(i)(1) and a certification as specified in 37 CFR § 1.97(e), as checked below. This document is to be considered as a petition requesting consideration of the information disclosure statement.

[If either of boxes (d) or (e) are checked above, the following "certification" under 37 CFR § 1.97(e) may need to be completed.] The undersigned certifies that:

(f) ☐ Each item of information contained in the information disclosure statement was cited in a communication mailed from a foreign patent office in a counterpart foreign application not more than three months prior to the filing of this information disclosure statement.

(g) ☐ No item of information contained in this information disclosure statement was cited in a communication mailed from a foreign patent office in a counterpart foreign application or, to the knowledge of the undersigned after making reasonable inquiry, was known to any individual designated in 37 CFR § 1.56(c) more than three months prior to the filing of this information disclosure statement.

A list of the patent(s) or publications(s) is set forth on the attached Form PTO-1449 (Modified). A copy of the items on PTO-1449 (Modified) is supplied herewith:

(h) ☒ each            (i) ☐ none            (j) ☐ only those listed below:

Those patent(s) or publication(s) which are marked with an asterisk (\*) in the attached form PTO-1449 (Modified) are not supplied because they:

- (1) were previously cited by or submitted to the Office in a prior application no. \_\_\_\_\_ filed \_\_\_\_\_ and relied upon in this application for an earlier filing date under 35 U.S.C. § 120 or
- (2) are hereby waived pursuant to 37 CFR 1.98 (a)(2)(i) for all U.S. national patent applications filed after June 30, 2003.

A concise explanation of relevance of the described above and attached information is:

(k) ☒ not given

(l) ☐ given for each listed item

(m) ☐ given for only non-English language listed item(s) [Required]

(n) ☐ is in the form of an English language copy of a Search Report from a foreign patent office, issued in a counterpart application, which refers to

the relevant portions of the references [copy attached].

While the information disclosed in this Information Disclosure Statement may be "material" pursuant to 37 CFR § 1.56, it is not intended to constitute an admission that the information referred to is "prior art" for this invention unless specifically designated as such.

In accordance with 37 CFR § 1.97(g), the filing of this Information Disclosure Statement shall not be construed to mean that a search has been made or that no other material information as defined in 37 CFR § 1.56(a) exists. It is submitted that the Information Disclosure Statement is in compliance with 37 CFR § 1.98 and MPEP § 609 and the Examiner is respectfully requested to consider the above-described and attached information.

The Commissioner is hereby authorized to charge our Deposit Account No. 502305 for any fees required in connection with the filing of the Information Disclosure Statement. **A duplicate copy of this Notice is enclosed for this purpose.** In particular, in the event that an Office Action has crossed in the mail with this Information Disclosure Statement, the Commissioner is authorized to charge the above-named deposit account for any fees required pursuant to 37 C.F.R. §1.17(p) or 1.17 (i) (1).

Respectfully submitted,

Dated: 2-19-04

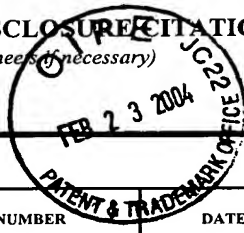
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Atty. Docket No: 100-22801 (P05122-C1)

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**INFORMATION DISCLOSURE CITATION**  
(Use several sheets if necessary)



Docket Number (Optional)

100-22801 (P05122-C1)

Application Number

10/041,112

Applicant(s)

Wipawan Yindeepol et al.

Filing Date

January 8, 2002

Group Art Unit

2815

**U.S. PATENT DOCUMENTS**

*EXAMINER INITIAL	REF	DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLASS	FILING DATE IF APPROPRIATE
		5,915,179	06/22/99	Etou et al.	438	268	06/07/96
		6,404,026 B2	06/11/02	Tsuyuki	257	409	12/27/00
		5,525,822	06/11/96	Vinal	257	344	05/01/95
		U.S. Appln. 10/042,811		Charles A. Dark et al.			01/08/02

**FOREIGN PATENT DOCUMENTS**

	REF	DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUBCLASS	Translation	
							YES	NO

**OTHER DOCUMENTS** (Including Author, Title, Date, Pertinent Pages, Etc.)

		Donald T. Comer, "Zener Zap Anti-Fuse Trim in VLSI Circuits", VLSI Design, 1996, Vol. 5, No. 1, pps. 89-100.
		P. Beaud et al., "0.5um Technology Development and Qualification", [online], retrieved on 2003-12-14]. Retrieved from the Internet <URL:http://legwww.epfl.ch/research/pdf/05umf.pdf>. pps. 1-2 (unnumbered).

EXAMINER

DATE CONSIDERED

EXAMINER: Initial if citation considered, whether or not citation is in conformance with MPEP Section 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.